

[illegible]

jc\$03 U.S. PTO
09/505215

APPLICANT'S ART CITATION (Use several sheets if necessary)		Application 09/506,215				OFGS File No. P/126-182					
		Applicant SHIMADA, N.									
		Filing Date 2/17/00				Group Art Unit 2756					
U.S. PATENT DOCUMENTS											
Examiner Initials	Document Number						Date	Name	Class	Sub- class	Filing Date If Appropriate
FOREIGN PATENT DOCUMENTS											
	Document Number						Date	Country	Class	Sub- class	Translation Yes No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)											
<input checked="" type="checkbox"/> ZM	*	Yuhei Iwasaki, "New LAN Introduction Method 9, Layer 3 Switch Construction," Computer & Network LAN, Vol. 16, No. 6, Ohm Company, Limited, pp. 87-90, June 1998.									
<input checked="" type="checkbox"/> ZM	*	Hiroshi Esaki: "High Speed Routing Technique and Multi-layer Switch Technique," BIT, Vol. 30, No. 5, Kyoritsu Publishing Company, Limited, pp. 83-91, May, 1998.									
<input checked="" type="checkbox"/> ZM	*	Yuhei Iwasaki, "New LAN Introduction Method 11, Cut-through Layer 3 Switch Construction," Computer & Network LAN, Vol. 16, No. 9, Ohm Publishing Company, Limited, pp. 57-61, September 1998.									
Examiner /Zarni Maung/							Date Considered 11/15/09				
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.											